

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

MATSUURA, *et al.*

Serial No.: 10/750,910

Filed: January 5, 2004

For: PREDISTORTION CIRCUIT, LOW-DISTORTION
POWER AMPLIFIER, AND CONTROL METHODS
THEREFOR

Art Unit: 2817

Examiner: H. Choe

Atty. Dckt: 033216M0752

REQUEST FOR CERTIFICATE OF CORRECTION UNDER 37 C.F.R. 1.322

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

ATTN: Certificate of Correction Branch

Dear Sir:

Applicants respectfully request the U.S. Patent & Trademark Office (USPTO) to issue a Certificate of Correction in U.S. Patent Application Serial No. 10/750,910, now U.S. Patent No. 6,960,958 under 37 C.F.R. 1.322.

The requested corrections are attached on Form PTO-1050.

Since all of the errors are ascribable to the USPTO, no fees are required.

If, however, any fees are required, such are authorized to be charged to **Deposit Account No. 02-4300**, Attorney Docket No. **033216M0752**.

Respectfully submitted,
SMITH, GAMBRELL & RUSSELL, LLP



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Date: March 24, 2006

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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PATENT NO. : **6,960,958**

APPLICATION NO.: **10/750,910**

ISSUE DATE : **November 1, 2005**

INVENTOR(S) : **Matsuura, et al.**

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

IN THE CLAIMS:

--2. The control method for the low-distortion power amplifier according to ~~any one of~~
~~claims 1~~ claim 1, comprising the steps of:

detecting a magnitude of the distortion signal that is output from the distortion
extraction circuit; and

controlling at least one of the amplitude frequency characteristic adjustment circuit
or circuits and the vector adjustment circuit or circuits so as to minimize the detected
magnitude of the distortion signal.--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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